

# Mixed Signal Circuit Design and Simulation Marathon using eSim

Greetings from the FOSSEE team!

The FOSSEE (<https://fossee.in/>) project, based at IIT Bombay along with [VLSI System Design Corp.Pvt. Ltd](#) and [Redwood EDA](#) invite you to participate in an online Mixed Signal Circuit Design and Simulation Marathon using [eSim](#). The purpose of this Marathon is to provide an open platform to students to innovate, create state-of-the-art design and integrate it into real-world applications

This marathon is based on eSim, an open-source EDA tool for circuit design, simulation, and PCB design, developed by FOSSEE, IIT Bombay. eSim is an open-source alternative to PSpice, Orcad, etc.

The details of the marathon, learning resources, guidelines, how to register, etc., can be found here: <https://hackathon.fossee.in/esim/>. Registrations for the marathon opened on 10 February 2022; the last date of registration is 24 February 2022.

Please find the poster of this event [here](#). You can download and use it. Feel free to share this information with friends/colleagues in other institutions/industries or anyone who you deem fit for this marathon. If you have any queries, you can write to us at [contact-esim@fossee.in](mailto:contact-esim@fossee.in).

**PFA: <https://drive.google.com/file/d/1J4Ks1lxKvsJ4sWoxqLn79n367Zul9dap/view?usp=sharing>**

**Regards,  
eSim Team  
FOSSEE, IIT Bombay**